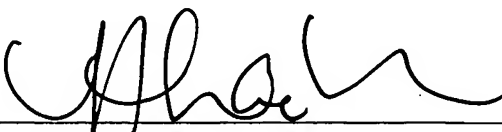




PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to Mail Stop Issue Fee, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

January 12, 2006   
Date \_\_\_\_\_ Ayesha J. Shaikh

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Greg A. Blodgett and  
Christopher K. Morzano

Attorney Docket No.: 501266.01

Serial No. : 10/726,312

Group Art Unit : 2819

Filed : December 1, 2003

Examiner : James Cho

Confirmation No. : 3071

Date of Notice  
of Allowance : October 20, 2005

Title : METHOD AND CIRCUIT FOR OFF CHIP DRIVER CONTROL, AND MEMORY  
DEVICE USING SAME

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Mail Stop Issue Fee  
Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**COMMENTS ON STATEMENT OF REASONS**  
**FOR ALLOWANCE**

Sir:

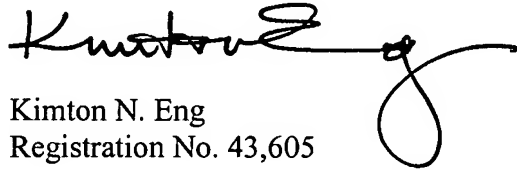
In the Examiner's reasons for allowance, he states each independent claim is allowable because the prior art does not teach or suggest a particular element or elements recited in each of the claims. While applicants agree with the Examiner's indication that the prior art does not teach or disclose the particular elements cited by the Examiner, the undersigned would like to point out that each claim includes a combination of elements and it is the recited

combination, which includes the elements cited by the Examiner, that is not disclosed nor suggested by the prior art.

While the combinations of elements recited in the allowed claims are allowable, the undersigned would like to point out that some or all of these individual elements may be broadened such that the resulting combination is still patentable. Applicants may elect to pursue such claims, or to pursue claims directed to other aspects of the present invention, through a continuation or reissue application, or through a reexamination proceeding.

Respectfully submitted,

DORSEY & WHITNEY LLP



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